



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Information Technology**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**B.Tech+MBA(CSE) and B.Tech+M.Tech(CSE)**

**SEMESTER - II ( 2023-2027)**

Sr. No.	Course Code	Course Name	TEACHING			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTMACS 201	Mathematics-II	3	1	0	4	60	20	20	0	0	100
2	BTEC104	Digital Logic & Circuit Design	3	1	2	5	60	20	20	30	20	150
3	BTCS202M	Object Oriented Programming with C++	3	0	2	4	60	20	20	30	20	150
4	BTIT201M	Data Communication	3	0	0	3	60	20	20	0	0	100
5	BTCS102M	Introduction to Design Thinking	2	0	2	3	60	20	20	30	20	150
6	BTIT208M	Unix Programming	0	0	2	1	0	0	0	30	20	50
			<b>14</b>	<b>2</b>	<b>8</b>	<b>20</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>120</b>	<b>80</b>	<b>700</b>

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

Chairperson  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

Controller of Examinations  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

Joint Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

Vice Chancellor  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

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